

Agenda

- Arm Software
- Debugging with DDT
- Theta Specific Settings

Arm Software arm © 2018 Arm Limited

Arm Forge

An interoperable toolkit for debugging and profiling



The de-facto standard for HPC development

- Available on the vast majority of the Top500 machines in the world
- Fully supported by Arm on x86, IBM Power, Nvidia GPUs, etc.



State-of-the art debugging and profiling capabilities

- Powerful and in-depth error detection mechanisms (including memory debugging)
- Sampling-based profiler to identify and understand bottlenecks
- Available at any scale (from serial to parallel applications running at petascale)



Easy to use by everyone

- Unique capabilities to simplify remote interactive sessions
- Innovative approach to present quintessential information to users

Run and ensure application correctness

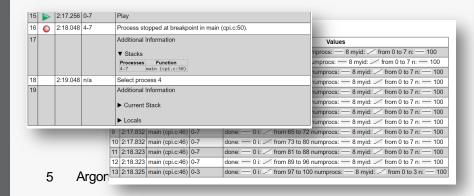
Combination of debugging and re-compilation

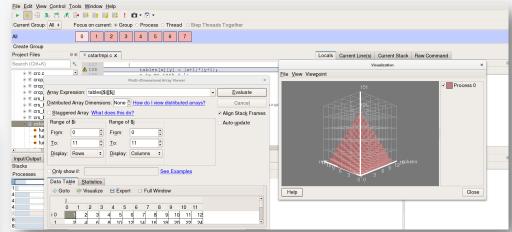
- Ensure application correctness with
- Integrate with continuous integration system.
- Use version control to track changes and leverage Forge's built-in VCS support.

Examples:

\$> ddt --offline aprun -n 48 ./example

\$> ddt --connect aprun -n 48 ./example





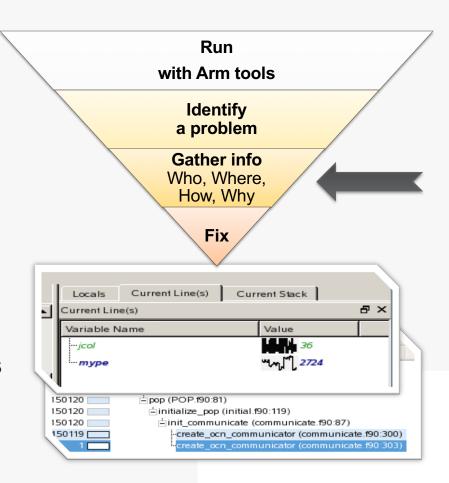


Debugging with DDT arm © 2018 Arm Limited

Arm DDT – The Debugger

Who had a rogue behaviour?

- Merges stacks from processes and threads Where did it happen?
- leaps to sourceHow did it happen?
- Diagnostic messages
- Some faults evident instantly from source Why did it happen?
- Unique "Smart Highlighting"
- Sparklines comparing data across processes



Preparing Code for Use with DDT

As with any debugger, code must be compiled with the debug flag typically -g

It is recommended to turn off optimization flags i.e. -00 Leaving optimizations turned on can cause the compiler to optimize out some variables and even functions making it more difficult to debug



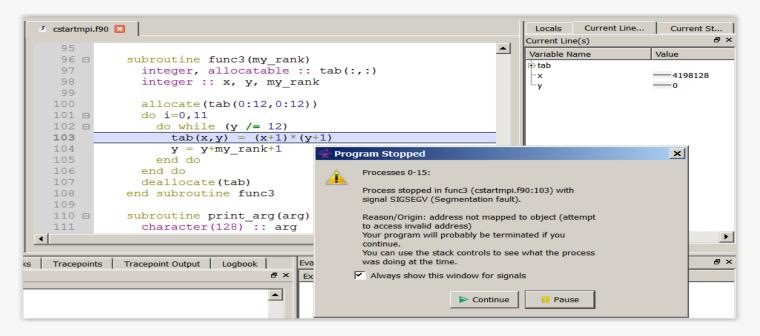
Segmentation Fault

In this example, the application crashes with a segmentation error outside of DDT.

What happens when it runs under DDT?



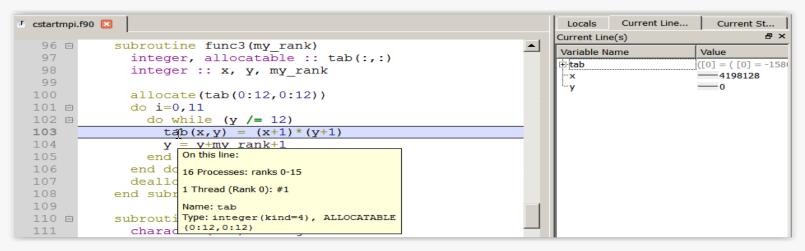
Segmentation Fault in DDT



DDT takes you to the exact line where Segmentation fault occurred, and you can pause and investigate



Invalid Memory Access



The array tab is a 13x13 array, but the application is trying to write a value to tab(4198128,0) which causes the segmentation fault.

i is not used, and x and y are not initialized



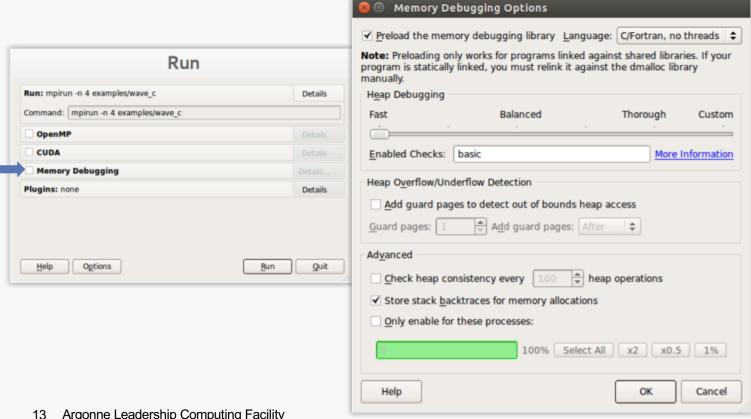
It works... Well, most of the time



A strange behaviour where the application "sometimes" crashes is a typical sign of a memory bug

Arm DDT is able to force the crash to happen

Advanced Memory Debugging





Heap debugging options available

Fast

basic

•Detect invalid pointers passed to memory functions (e.g. malloc, free, ALLOCATE, DEALLOCATE,...)

check-fence

 Check the end of an allocation has not been overwritten when it is freed.

free-protect

 Protect freed memory (using hardware memory protection) so subsequent read/writes cause a

Added goodiness

 Memory usage, statistics, etc. Balanced

free-blank

 Overwrite the bytes of freed memory with a known value.

alloc-blank

 Initialise the bytes of new allocations with a known value.

check-heap

 Check for heap corruption (e.g. due to writes to invalid memory addresses).

realloc-copy

 Always copy data to a new pointer when re-allocating a memory allocation (e.g. due to realloc) Thorough

check-blank

 Check to see if space that was blanked when a pointer was allocated/freed has been overwritten.

check-funcs

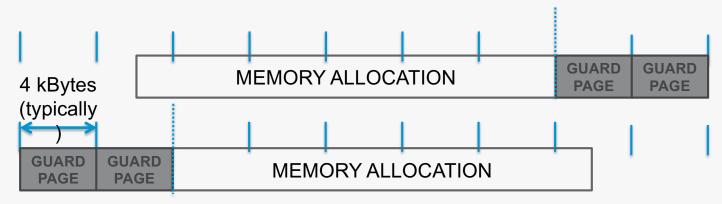
• Check the arguments of addition functions (mostly string operations) for invalid pointers.

See user-guide:

Chapter 12.3.2



Guard pages (aka "Electric Fences")



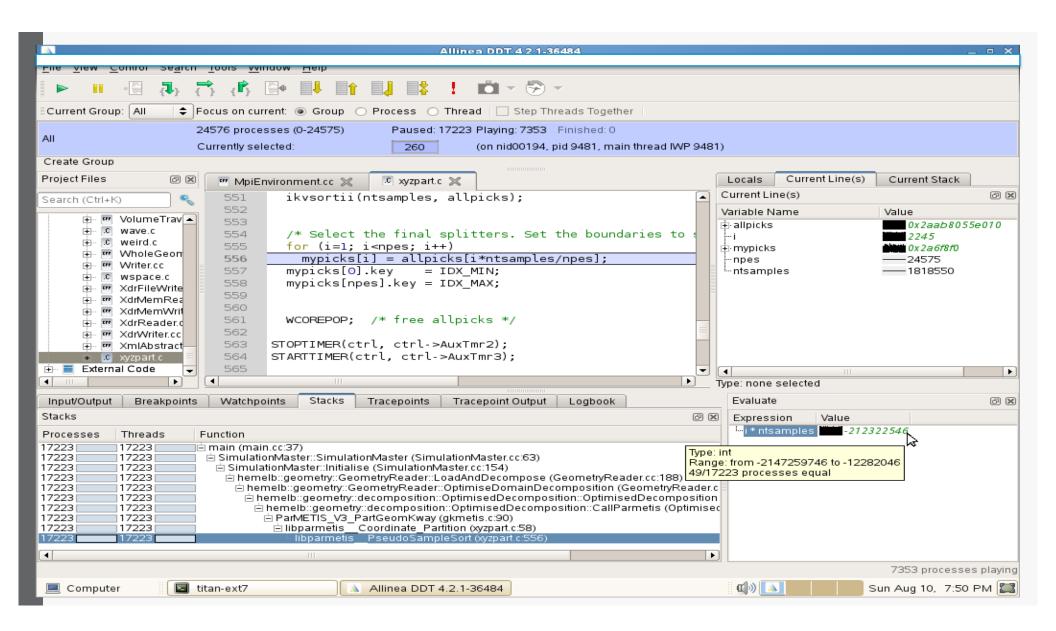
A powerful feature...:

Forbids read/write on guard pages throughout the whole execution (because it overrides C Standard Memory Management library)

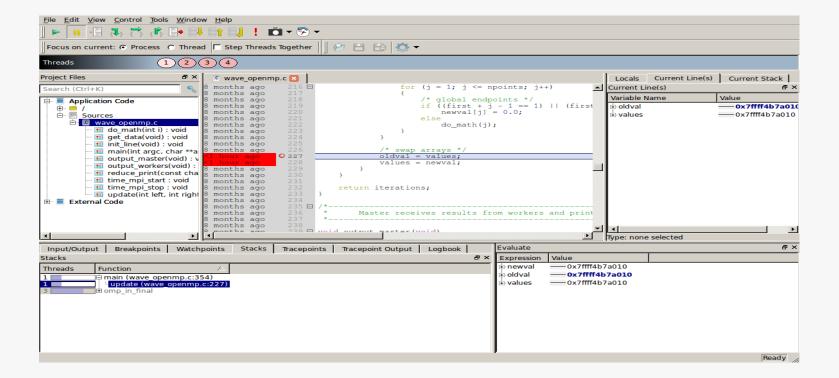
... to be used carefully:

- Kernel limitation: up to 32k guard pages max ("mprotect fails" error)
- Beware the additional memory usage cost



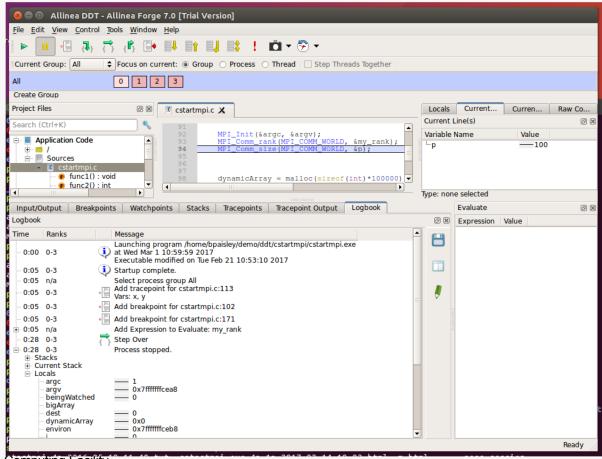


New Bugs from Latest Changes



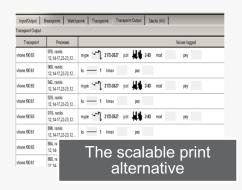


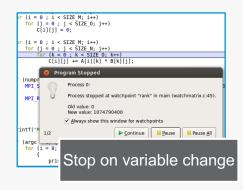
Track Your Changes in a Logbook

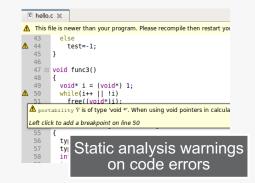


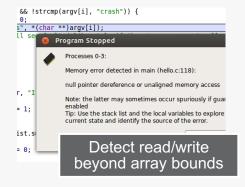
Arm DDT Demo arm © 2018 Arm Limited

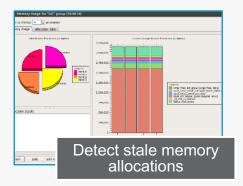
Five great things to try with Allinea DDT













Arm DDT cheat sheet

Load the environment module

- \$ module load forge/19.1.2
- \$ module unload xalt

Prepare the code

\$ cc -O0 -g myapp.c -o myapp.exe

Start Arm DDT in interactive mode

\$ ddt aprun -n 8 ./myapp.exe arg1 arg2

Or use the reverse connect mechanism

- On the login node:
 - \$ ddt &
- (or use the remote client) <- Preferred method
- Then, edit the job script to run the following command and submit:
 - ddt --connect aprun -n 8 ./myapp.exe arg1 arg2



Theta Specific Settings arm © 2018 Arm Limited

Configure the remote client

Install the Arm Remote Client

Go to : https://developer.arm.com/products/software-development- tools/hpc/downloads/download-arm-forge

Connect to the cluster with the remote client

- Open your Remote Client
- Create a new connection: Remote Launch → Configure → Add
 - Hostname: <username>@theta.alcf.anl.gov
 - Remote installation directory:

/soft/debuggers/forge-19.1.2-2019-08-06



Static Linking Extra Steps

To enable advanced memory debugging features in DDT, you must link explicitly against our memory libraries Simply add the link flags to your Makefile, or however appropriate

Iflags = -L/soft/debuggers/ddt/lib/64 -WI,--undefined=malloc -ldmalloc -WI,--allow-multiple-definition



Questions? arm © 2018 Arm Limited

Thank You! Danke! Merci! 谢谢! ありがとう! **Gracias!** Kiitos! 감사합니다 धन्यवाद

